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Power Quality Enhancement of Grid Integrated Distribution System with Renewable Energy Sources

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ABSTRACT

Nowadays, Renewable Energy Sources (RES) integration to Distribution Systems (DS) by employing latest Power Electronic Converters (PEC) is highly increased. This paper presents, a new grid connected / interfacing inverter control organization for a 3-Ø 4-wire DS. The implemented inverter is incorporated with Active Power Filter (APF) functionality which can control and perform as a multi-functional device. So, the inverter is operated as both power converter and Shunt APF as well. The power converter function is to inject the real power into the grid, whereas the shunt APF is to minimize reactive power demand, current harmonics and unbalance current. With the effective inverter control scheme, the 3-phase 4-wire DS at PCC seems as balanced load to grid. The inverter and its control scheme are implemented in using MATLAB /Simulink environment.

Key words : APF; Voltage Source Inverter; Power Quality; RES..

1. INTRODUCTION

The anonymous increase of electrical demand in the modern civilization, researchers are searching towards the clean energy development without effecting the environment. In this aspect, the RES are the most promising alternatives to others. On the other hand, it is not viable to carry bulk amount of power from remote stations to load centers. Then there is an evolution of the term Distributed Generation (DG) which means generation at load centers [1],[2]. The RES are intermittent in nature, whereas the load on the system is not constant. So, to integrate RES in to the existing grid sophisticated control strategies are required.

In the modern power system, power generation units are suffered from two limitations such as lack of fossil fuel and carbon emission. Moreover, an electrical power loss due to transmission lines is another constraint. So these consequences driving fossil fuel based power generation units to DG with RES in to the grid. This results in efficient energy, reduced power losses and emissions. Integration RES to the medium to low voltage power transmission system is becoming good research platform. Almost, the integration of RES could be done with PECs. While integrating DG to the grid and to meet the power quality standards, mainly power electronic converters is used. Exponential growth is with DGs such as grid connected systems with number of PV and wind installations. Besides them, the main drawbacks of the systems are low efficiency and controllability, if none controlled properly, lead to grid collapse. The integration of RES is a reason of power quality issues to the grid. Many studies and researches have already noticed the impact of RES penetration on the distribution system [3],[4]. A simple reason behind unfavorable effects on the grid is, leaving the DG on its own. It requires proper counteracting solutions and superior control schemes.

The paper sections are arranged as follows: The section II defines the several grid interfacing technologies available to integrate DG. Section III explains about the architecture of the proposed system. The main importance of DC link between the RES and inverter with the help of mathematical equations is explained in Section IV. Section V presents the proposed control scheme with PI & Hysteresis current controller. Sections VI concentrates on the simulation results.

2. DIFFERENT TYPESOF GRID INTERFACES

As per the grid codes, the RES based generators are not directly integrated to the grid. It requires medium voltage level conversion/transmission media in between RES generators and grid. In case of solar PV panels, a dc-to-ac converter is necessary to integrate the grid. Subsequently, in case of small hydro or wind generators can be directly integrated to the grid which creates low conversion efficiency, poor power quality and starting transients. Therefore, PECs are requiring as a best choice for integrating variable RES. Table 1 describes about the RES types of generations and their preferred interfacing technology to grid [5].

_	Table 1 Types	of g	generations	and	their	preferred	interfacing

Type Interfacing	Technology			
Photovoltaic	PECs			
Wind	Induction generator/ PECs			
Small hydro	Induction or synchronous generator, PECs			
Fuel cells	PECs			

Mostly, current controlled VSIs are utilized to integrate RES in DS. Nevertheless, the wide-ranging use of PECs and unbalanced loads at PCC may deteriorate the power quality. Nowadays, the p-q theory based grid integrated inverters are proposing with several control strategies. To absorb the harmonic current, at a certain frequency the inverter operates as active inductor. Nevertheless, in real time, obtaining the network inductance is difficult which may deteriorate performance of a controller. So, to overcome such situation, shunt APF is proposed to damp out the harmonics in DS, because it acts as active conductance. Finally, p-q theory based strategy is proposed for grid integrated inverter. To minimize the current harmonics of the load, load and inverter current sensing is necessary for the control strategy. In this strategy, the control scheme employs Fuzzy Logic Controller (FLC) which has more advantages [6].

3. SYSTEM DISCRIPTION

The schematic representation of proposed RES based DG system is shown in Figure 1, which mainly comprises of RES, DC-link, 4-leg grid interfacing inverter, hysteresis controller and a distribution grid. The VSI is a main interface in between the RES and grid. The RES is a DC/AC source depending on the energy source (solar or wind). However, a rectifier is coupled with DC-link. In general, variable speed wind generators produce variable AC voltage, whereas photovoltaic and fuel cell generates variable low DC voltage. Therefore, to connect a RES with DC link a power conditioning devices (ac/dc or dc/dc converters) are essential. The DC-link divides RES and grid which gives advantages such as no fault transfer and independent control of converters [7].



Figure 1: Schematic representation of RES based DG system

4. CONTROL OPERATION OF DC-LINK VOLTAGE AND POWER

The power generated by RES is not constant due to their intermittent nature. For integration of renewables in to grid

for power transfer DC link plays a main role. While, implementing the DC link the RES is treated as current source connected to the DC side of the inverter. The basic connection of RES to grid is depicted in Figure 2. The current source is an equivalent value of RES current generation, if the RES is an photovoltaic system the output current depends on and temperature of the solar radiation. Since, the output power is DC, no need of extra converter for conversion. If the connected DG source is a wind turbine the power from the alternator is an alternating quantity [8]. This AC must be converted in to equivalent DC power by means of a converter, for mathematical analysis in terms of DC. It contains current source equivalent of RES and DC link capacitor. The Eqn. (1) represents the current injected from RES to DC link at voltage V_{dc} .

$$I_{de\,1} = \frac{P_{RES}}{v_{de}} \tag{1}$$

where, P_{RES} is the total DC output power from RES. The power balance equation at PCC is given by Eqn. (2).

$$P_{Inv} = P_G + P_{lass} \tag{2}$$

where Pinv is power available at inverter terminals on grid side, PG is total real power supplied and Ploss is total loss in the inverter. The current delivered by the DC link is shown in Eqn. (3).

$$I_{de\,2} = \frac{P_{fav}}{V_{de}} = \frac{P_G + P_{lass}}{V_{de}}$$
(3)

By neglecting the inverter losses, the total power produced by RES and supplied to grid through inverter is expressed by Eqn. (4).

$$P_{RES} = P_{G} \tag{4}$$



Figure 2: Equivalent diagram of DC-Link

5. GRID INTEGRATING INVERTER CONTROL

The proposed controller for integration of inverter to 3-phase with neutral system is depicted in Figure 3. The inverter in this configuration contains four legs each for three phases and neutral. To compensate the load neutral current when the system is unbalanced, the fourth leg is used. The controller is designed in such a way that it regulates the power transfer to the PCC during the following conditions of power availability of RES. (i) $P_{RES} = 0$, (ii) $P_{RES} < P_L$ and (iii) $P_{RES} > P_L$. The term P_L denotes the total load power requirement [12],[13],[14].

The controller is designed which deliver active component of power to grid to ensure the power efficient operation and power quality. If load is connected to PCC, any unbalanced or combination of the former occurs, the inverter controller will mitigate harmonics and makes the neutral current balancing. The unbalanced load treated as balanced resistive load by using switching pulses and duty cycle of the inverter [9].

The DC link voltage regulation provides the active power flow between RES and the grid. The component of active current is produced by DC voltage regulator. To ensure grid currents are balanced, the reference value of neutral current I_n^* is usual to zero. Meanwhile, the voltage vector synchronizing angle θ is provided by the PLL.

To switching ripples in the DC voltage can be eliminated with the inclusion of filter into control circuit, the measured DC link voltage V_{dc} is given first-order low pass filter for the former mentioned operation. The PI controller is inserted to attain dynamic / unexpected voltages near the dc-link [10]. The DC link nth sampling instant voltage error $V_{dcerr(n)}$ and PI regulator output is expressed by Eqns. (5 & 6):

 $\mathbf{V}_{\mathrm{dcerr}(n)} = \mathbf{V}_{\mathrm{dc}(n)}^{*} - \mathbf{V}_{\mathrm{dc}(n)}$ (5)

 $I_{m(n)} = I_{m(n-1)} + 10 (V_{dcerr(n)} - V_{dcerr(n-1)}) + 0.05 V_{dcerr(n)}$ (6) The sudden changes in grid currents are obtained by Eqns from (7-9).

$$\mathbf{I}_{a}^{*} = \mathbf{I}_{m}. \ \mathbf{U}_{a} \tag{7}$$

$$\mathbf{I}_{\mathbf{b}}^{*} = \mathbf{I}_{\mathbf{m}}. \ \mathbf{U}_{\mathbf{b}} \tag{8}$$

$$\mathbf{I}_{\mathrm{c}}^{*} = \mathbf{I}_{\mathrm{m}}. \ \mathbf{U}_{\mathrm{c}} \tag{9}$$

Where $I_n^* = 0$ because of current balancing. I_a^* , I_b^* , I_c^* are corresponds to reference currents in a, b, c phases.

The reference and actual currents of grid (I_a , I_b , $I_c \& I_n$) are compared to calculate the errors in current with the help of Eqn. (10-13) in order to produce PWM pulses for the inverter [11].

$$\mathbf{I}_{aerr} = \mathbf{I}_{a}^{*} - \mathbf{I}_{a} \tag{10}$$

$$\mathbf{I}_{\text{berr}} = \mathbf{I}_{\text{b}} - \mathbf{I}_{\text{b}} \tag{11}$$

$$I_{cerr} = I_c - I_c$$
(12)
$$I_c = I^* - I_c$$
(12)

$$\mathbf{I}_{\text{nerr}} = \mathbf{I}_{\text{n}} - \mathbf{I}_{\text{n}} \tag{13}$$



Figure 3: Control strategy of grid-integrating inverter

The hysteresis current controller is fed by current errors for the inverter PWM pulses generation, to deliver power from RES [11]. The average model of the inverter with neutral connection is given by Eqn. (14) - (18).

$$\frac{u_{Inva}}{d\tau} = \frac{v_{Inva} - v_a}{L}$$
(14)

$$\frac{dI_{hub}}{dI_{hub}} = \frac{V_{hub} - V_b}{V_{hub}}$$
(15)

$$\frac{u_1}{u_{hve}} = \frac{v_{hve} - v_e}{v_{e}}$$
(16)

$$\frac{dt}{dt} = \frac{1}{L_{th}} \tag{17}$$

$$\frac{dV_{dr}}{dV_{dr}} = \frac{I_{haved} + I_{haved} + I_{haved} + I_{haved}} \tag{10}$$

$$\frac{dr}{dt} = \frac{\Gamma_{avad} + \Gamma_{avad} + \Gamma_{avad}}{C_{dr}}$$
(18)

where, V_{Inva} , V_{Invb} , V_{Invc} , V_{Invn} and I_{Inva} , I_{Invb} , I_{Invc} , I_{Invn} are the voltages and currents at the inverter output terminals a,b,c and neutral. The expressions for terminal voltage are as shown Eqn. (19) – (22).

$$V_{Inva} = \frac{P_1 - P_4}{2} V_{dc}$$
(19)

$$V_{Invb} = \frac{r_3 - r_6}{P_2 - P_2} V_{de}$$
(20)

$$V_{Inve} = \frac{P_{5} - P_{2}}{2} V_{de}$$
(21)
$$V_{1} = \frac{P_{7} - P_{1}}{2} V_{de}$$
(22)

$$V_{Invn} = \frac{1}{2} V_{de}$$
(22)
Similarly, the charging currents (L. 1, L. 1, L. 1)

Similarly, the charging currents (I_{Invad} , I_{Invbd} , I_{Invcd} , $I_{Invndon}$) in terms of inverter phase currents and switching pulses are given by Eqn. (23) – (26).

$$\vec{I}_{Invad} = \vec{I}_{Inva} (\vec{P}_1 - \vec{P}_4)$$
(23)

$$I_{Invbd} = I_{Invb} (P_3 - P_6) \tag{24}$$

$$I_{Inved} = I_{Inve} \left(P_5 - P_2 \right) \tag{25}$$

$$I_{Invnd} = I_{Invn} (P_7 - P_n)$$
(26)

The sequence of switching inside IGBT inverter has done on the bases of deviation of current from its actual value. The possible conditions depending on the value of error are as follows:

- 1. If $I_{Inva} < (I_{Inva}^* h_b)$, then $P_1 = 0$ pulse for S_1 is disconnected and S_4 will be active ($P_4 = 1$).
- 2. If $I_{Inva} > (I_{Inva}^* h_b)$, then $P_1 = 1$ pulse for S_1 is in active and vice versa for S_4 .

where the hysteresis band width of hysteresis controller is denoted by h_b .

6. THE MATLAB/SIMULINK MODEL OF THE GRID INTERCONNECTED RES

The performance evaluation of the propose system with 3-phase 4-wire configuration with various objectives is evaluated by using MATLAB/Simulink. Figure 4 shows control scheme with conventional PI-controller the current controlled VSI with the proposed controller maintaining Unity Power Factor and balancing the neutral current there by improving the performance of the system when operating with nonlinear unbalanced loading at PCC. Figure 5 shows the usage of DC link that improves the active power deliver to grid though the source of energy is intermittent in nature. Figure 6 shows the proposed controller actively, mitigating

harmonics caused by nonlinear loads, reactive power compensation at PCC. The grid voltages (V_a, V_b, V_c) and currents (I_a, I_b, I_c) with non-linear loads when RES is not connected to grid.



Figure 4: Proposed control scheme with conventional PI-controller



Figure 5: Proposed system with grid interconnection



Figure 6: Developed system model in Simulink to measure grid, load, inverter voltages, currents, active and reactive powers.

7. SIMULATION RESULTS

Figure 7 shows Voltage V_{abc} , Currents I_{abc} waveforms of grid, dynamic load (I_{labc}) and inverter currents (I_{invabc}).



Figure 7: Voltages & currents of the grid, dynamic load and Inverter currents.



Figure 8: Grid real & reactive powers, load and inverter

Figure 8, shown the grid active and reactive powers, load and inverters. The rise in waveform of powers indicates the power flow from grid and inverter to PCC. The +ve sign directs the real and reactive power disbursed by the load. Initially, the total load supply demand is zero because the grid-interfacing inverters will not be interfaced to the system. So, up to t=0.72 s, the grid supplies the load demand which indicates that, grid current is equal to load current. From t=0.72 s, the inverter interfaces the network and grid which results in inverter starts injecting current to the grid. Subsequently the grid current profile changes to a balanced sinusoidal current from unbalanced nonlinear current.

The direction of active and reactive power flows to grid, load, inverter and DC-link voltage at t=0.72 s are shown in Figure 9. From t=0.72 s, the inverter begin to supplying active power

to grid. If the RES produced power is higher than demand of load, the surplus amount will be supplies to grid which can be observed after t=0.72s (negative sign of P_{grid}).



Figure 9: Real and reactive power flows to the grid, load and inverter along with dc-link voltage

Therefore, after inverter interfacing the grid and network, the grid either can supplies/receives the real power depending on the load demand. From, t=0.82 s, the real power generation is improved for performance evaluation of the inverter control scheme under variable RES conditions which results in increased inverter current. At this instant, if the load demand is constant, excess RES power generated will be supplied to the grid, it is noticed at enhanced inverter current. From t=0.92 s, the power generated from RES is decreased, which results in corresponding change in inverter & grid current and real, reactive power from inverter to load as well in grid. Moreover, the DC-link voltage needs to be maintained constant in any operating condition for smooth power flow.

8. CONCLUSION

In this paper, a 3-phase 4-wire DS is presented. The grid interfacing inverter control is implemented and tested at various load conditions. The controller enables the injection of power in to the grid with maximum utilization of the RES included in the system. The simulation results shown that, grid-interfacing VSI is efficiently operated as power controlling equipment without affecting the transfer of active power. The control scheme of VSI is implemented to transfer the active power flow to grid and to operate as shunt APF. The integrated inverter suppressing, all the harmonics and neutral currents injected by the non-linear loads efficiently. To enhance the power quality, this control scheme doesn't require the auxiliary power conditioning equipment. Further improvements in the work will be carried out by adding AI to the system for the better understanding of faults occurred on the system.

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