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A Review on Quadratic Boost Topology-Based PFC Techniques

Salman Akram Memon¹, Abdul Hakeem Memon¹, Ashfaque Ahmed Hashmani¹

¹Research Student, ME (Electrical Power), IICT, Mehran UET Jamshoro, Pakistan,

salmanmemon9541@gmail.com

¹Assistant Professor (Department of Electrical Engineering),IICT, Mehran UET Jamshoro, Pakistan,

hakeem.memon@faculty.muet.edu.pk

¹Professor (Department of Electrical Engineering), IICT, Mehran UET Jamshoro, Pakistan,

ashfaque.hashmani@faculty.muet.edu.pk

ABSTRACT

Electronic devices require DC voltage for their operation so rectification (AC to DC conversion) is needed to convert AC voltage from the mains to DC. During this conversion, harmonics are generated in the system as well as power factor (PF) gets poor hence degrading power quality. Power factor correction (PFC) converters are being commonly used to increase PF and reduce harmonics. Boost PFC Converter is the mostly used topology for active power factor correction because of several benefits like series input inductor, high PF, but its voltage gain is not that good and it has output voltage ripple. Quadratic Boost PFC Converter can be used to overcome these two drawbacks by providing high voltage gain and less output voltage ripples. This paper represents applications and topologies of Quadratic Boost Converter, latest control techniques to achieve high voltage gain, regulated output and, high PF. Also, comparison is made between Boost Converter and Quadratic Boost PFC Converter. Finally, conclusion is made of this survey.

Key words:Quadratic boost, Boost, PFC converter, Harmonics, Voltage gain, Output voltage ripple, PF, Quadratic boost applications, Quadratic boost topologies

1. INTRODUCTION

The use of electronic devices is increased with the advancement of technology. Electronics need DC voltage for the operation so AC to DC conversion is required. Power electronic converters are used in these devices for conversion purpose. Main application of power electronics includes DC-DC converters that are used in industrial, commercial and residential application providing regulated output. Diodes and thyristors used in power electronics converters give controlled and semi controlled DC output. Due to non-linearity of diode and thyristor, there are several problems like distorted currents are drawn from the line when connected to ac mains that result in low PF and high total harmonic distortion (THD). These distorted currents cause several problems such as voltage distortion, heating, noise and power loss [1], [2], [3].So, this harmonic content produced by non-linear elements of electronic devices connected to AC supply system should be in such limit that it meets regulatory standard. Harmonic content can be reduced by active and passive method of power factor correction (PFC), passive method uses passive components with bridge diode circuit whereas active method uses switches like MOSFET and IGBTs. The requirement of

meeting regulatory standard is fulfilled by using active PFC circuits that with the help of switching devices shape the line current to be sinusoidal in nature and follow the wave form of line voltage. PFC converters are being commonly used in AC to DC power conversions to get PF near to unity and to reduce harmonic distortion so as to meet the standards like IEC61000-3-2 and IEEE 519 [4]. Along with high PF and low THD, there is a need of output voltage with low ripple in PFC converter like in LED lightning driver and programmable output adopter for mobile devices and so on which is not possible by using Boost PFC converter [5]. Power factor (PF) near to unity and good voltage regulation can be obtained by using two stage PFC approach shown in figure 1 [1].



Figure 1: Basic two stage PFC converter circuit

2. STUDY OBJECTIVES

This paper aims to present topologies and application of Quadratic Boost PFC converter. Different controlling techniques to improve converter's performance, voltage regulation and power factor presented by many authors have been discussed. Detailed comparison has also been made between Boost and Quadratic Boost PFC Converter.

3. METHODOLOGY

3.1 Comparison between Boost and Quadratic Boost PFC Converter

The comparison between Boost and Quadratic Boost PFC Converter is discussed here in terms of their input and output characteristics, design construction, voltage gain an defficiency. The input inductor peak current of Quadratic Boost PFC is smaller compared to Boost PFC converter [6]. Boost PFC converter has smooth continuous input current waveform due to series inductor in input side [7] whereas

Quadratic Boost PFC converter has smoother continuous wave form due to two inductors in input side. The study in [6] also shows that Quadratic Boost PFC converter reduces output voltage ripple, but it has more THD compared to Boost PFC converter. Boost PFC Converter is used for stepup applications due to its simple design and low cost but it is not well suited for highstep-up applications because of low voltage gain. To get high voltage gain from Boost PFC Converter, its power switch should have high duty cycle that results in high conduction losses degrading converter's efficiency. Another simple way to obtain high gain without increasing duty cycle is to cascade boost converter which requires more switches and results in complex design and complex circuit. Using Quadratic Boost PFC converter, high voltage gain can be obtained which is a two-stage boost converter with single switch topology overcoming the disadvantage of conventional Boost PFC Converters [8], [9], [10]. The study in [11] shows Quadratic Boost PFC Converter has more efficiency compared to Boost PFC Converter, because there are high conduction and switching losses as current of each boost stage flows from the single common switch. It is also because Quadratic Boost PFC Converter is made from cascaded connection of two boost converters [5], [9]. The conclusion of this discussion is given in the table 1.

Table 1: Comparison of boost and quadratic boost PFC parameters

Parameters	Boost PFC Converter	Quadratic Boost PEC	Reference
	Converter	Converter	
Input	Smoothcontinuous	Smoother	[6], [7].
_	current waveform,	continuous	
	more input	current	
	inductor peak	waveform,	
	current, more	less input	
	THD	inductor	
		peak	
		current, less	
		THD	
Output	High output	Higher	[6], [8].
	voltage, more	output	
	output voltage	voltage, less	
	ripples	output	
		voltage	
		ripples	
Voltage	Low	High	[8], [9],
Gain			[10].
Design	Simple	Complex	[8], [9],
_	_	_	[10].
Efficiency	High	Low	[5], [9],
			[11].

4. APPLICATION OF QUADRATIC BOOST CONVERTER (QBC)

DC-DC converters with high gain have become popular due to their use in solar photovoltaic (PV) system and electric vehicles (EV) [12]. QBC is used for the applications when high voltage gain is required like PFC and PV applications [13]. This section presents literature review of QBC used in application of EV, PV cell and PFC.

4.1 Photovoltaic (PV) Cell

Solar PV is the environment friendly technology because it generates electricity with no emission in the atmosphere as well as having very low maintenance cost [14]. PV panel's output is lower than needed by inverter for PV applications therefore this voltage needs to be increased. The requirement of high voltage can be achieved by connecting PV panels in series but with certain limitations such as shadowing effect, efficiency consideration etc. [13]. Boost converter, cascade boost converter, buck-boost converter, sepic converter and cuk, converter are usually used to increase the voltage gain. However, these converters are not efficient enough to produce high voltage due to switching losses and high ripple in current. Boost and Buck-boost converter can provide high voltage gain with very high duty cycle but it increases component stress as well as causes switching loss [15]. The simplest method to obtain high voltage gain is using QBC [9]. Authors in [10], [15], [16] use QBC in photovoltaic application. In [10] QBC with high voltage gain is presented which can track maximum power point (MPP) from PV module using incremental conductance (IC) algorithm under varying operating conditions. Action of getting maximum power from PV modules is known as maximum power point tracking (MPPT). In [15] QBC with switch capacitor is presented which gives reduced voltage stress and high conversion ratio with same value of duty cycle and input voltage. This converter is then connected after PV panel to charge the battery of 1000W, 48V. It was found from experimental results that even for the value of low voltage obtained from PV panel converter produce enough charging voltage to the battery. In [16] 100W, QBC with energy storage system is presented for low PV application by using PI controlled technique. It solves the intermitted power problem from output of PV panel by connecting battery in parallel with capacitor of first stage of OBC, the battery gets charged and discharged to maintain constant power at load side. PI controller is used to obtain regulated voltage at output side received from PV panel. Low PV applications are PV power telecommunication system and residential application. Block diagram of PV panel charging the battery is shown in figure 2 in which voltage is boosted with the help of QBC, an inverter can also be connected after battery to converter DC into AC for supplying the electrical loads.



Figure 2: Block diagram for PV panel charging a battery

4.2 Electric Vehicle (EV)

Fuel cell, battery and ultra-capacitors can be used as energy source for Electric Vehicle's propulsion system as shown in figure 3. However, fuel cell (FC) is traditionally used as primary source of energy in electric vehicles, and it provides pollution free and efficient operation compared to vehicles using internal combustion engines which burns diesel and petrol for its propulsion system [17]. Fuel cell based Electric Vehicles have two conversion stages; DC-DC and DC-AC. DC-DC conversion can be obtained by using different topologies such as full-bridge, forward, flyback converter that results in high DC voltage and DC-AC converter is obtained by using an inverter [18]. Both of these conversions are shown in figure 3; a QBC is performing DC-DC conversion. The main problem in fuel cell based electric vehicle is that FC produces high current and low voltage. Normally, FC has a voltage of 1.1V. To obtain high voltage, FC stack is used, formed by connecting FCs in series however it increases cost as well as vehicle's weight. For fuel-cell based vehicles high voltage gain is needed, conventionally used boost converter cannot provide high gain due to losses related with inductor, capacitor and diode degrading converter's efficiency. Cascaded boost converter also gives high gain but results in complex converter design and more switches leading to more losses. Interleaved boost converter used with FC draws current no ripple but it has limited voltage gain. Multi-level DC-DC converters are capable of providing high voltage gain but having problems like complex structure and large size for EV application [19], [20]. QBC provides high voltage gain with tradeoff between efficiency and operation range of duty cycle compared to other topologies [21]. Authors in [17], [22], [23] use QBC with fuel cell in electrical vehicle application. In, [17] QBC with sliding mode-controlled technique is used to boost the output voltage of fuel cell and the result shows that this controller provides regulated voltage at DC link during changing load condition as needed by automobile application. In [22] QBC is presented for fugh tfflltopology, control action is obtained by connecting the switch after applications with no current and voltage stress on switch with high efficiency. In [23] interleaved OBC is designed for application of fuel cell and coupled inductor is used to improve converter's performance and efficiency.



Figure 3: Architecture of Electric Vehicle (EV)

4.3 Power Quality Improvement

Along with producing high output voltage, another usage of QBC is power factor correction (PFC) circuit. This converter has series inductor at input due to which it is suitable for power factor correction applications. The converter can be designed to be operated in four modes: CCM-CCM,CCM-DCM, DCM-CCM and DCM-DCM. For low power applications, converter is operated in discontinuous conduction mode (DCM) whereas for high power applications it is operated in continuous conduction mode (CCM). Among these modes, In DCM, the converter can automatically be operated as power factor correction circuit because it works as natural current shaper when being operated in DCM mode and no control loop is required but required in case of CCM [24], [5] It is also mentioned in [25] DC-DC converters when being operated in DCM have the ability of self- power factor correction because inductor's peak current samples the line voltage automatically. In addition to self-power factor correction, DCM has better features: constant frequency switching (beneficial for inductor design and EMI filter), zero current turning off on switch, and no reverse recovery of diode compared to CCM in which diode suffers from reverse recovery and has hard switching. However, in CCM inductor ripple current is very small that results in low RMS current on inductor and switch leading to high efficiency [26], [27]. Inductor's current when QBC is operated in DCM is shown in figure 4.



Figure 4: Inductor's current in DCM [24]

5. ANALYSIS

5.1 Quadratic Boost PFC Converter Topologies

5.1.1 Conventional Quadratic Boost PFC Converter

second inductor as shown in figure 5. Different control techniques are used [5], [28], [29] to improve its input PF and load and line voltage regulation.



Figure 5: Conventional Quadratic Boost PFC topology

The study in [4] uses 100-240V, 100W prototype being operated in DCM-DCM and variable duty cycle control (VDC) technique is used to improve converter's PF which is not good when duty cycle is constant. When duty cycle is constant, the PF is 0.9837 and 0.9249 for voltages 110V and 220V respectively. The PF is improved to 0.9975 and 0.9905 for voltages 110V and 220V respectively when duty cycle is variable. In this paper [28] 100W prototype is used with proportional integral derivative (PID) controller and fractional order proportional integral derivative (FOPID) controller for load and line variation. It was found that, load and line variation conditions give better steady state response but it has poor dynamic response with integer order PID controller where as FOPID controller's response during line and load fluctuations is better and it also give better transient and steady state responses. The authors in [29] use sliding mode controlled-technique for 100W prototype that gives quick regulated output voltage for both line and load regulation. It was also found that for line regulations sliding mode controller gives required value of output voltage quickly compared to PID controller with no overshooting. The experimental results show that in case of line regulation when input voltage is changed from 9.8V to 18.2V the output comes to a desired voltage of 79V within 2.25s and 7.5s respectively for both voltages. For load variation of 56.5 ohm and 69.278 ohm, the value of output voltage comes to 79V within 4.5s and 4.4s respectively. In this paper [9] 300V 120W prototype is used with high voltage gain by using coupled inductor and reduced voltage stress by using clamping circuits. Reduced voltage stresses don't allow additional power losses to occur that increases converter's efficiency. The authors in [30] analyze the operation of quadratic boost converter by using prototype of 12-48 V, 100W and it was observed based on experimental results that converter operates well enough throughout the range of its operation and gives highest efficiency of 83%.

5.1.2 Interleaved Quadratic Boost PFC Converter

This topology consists of switches, diodes and inductors connected in parallel resulting in parallel connected quadratic boost converter as shown in figure 6. Interleaving of converters make them suitable for high power applications and results in better efficiency, fast switching, and output voltage with less ripple and reduced inductor current ripple allowing use of small size filters [23], [31], [32].



Figure 6: Interleaved Quadratic Boost PFC topology

Authors in [23] use 40-57V, 2.5kW prototype for fuel cell applications and examined it for different structures of inductor, also the input of 40-57 (common in FC stacks) has been boosted to 400V using proposed converter. Further, their designed coupled inductor improves converter's efficiency and performance. Authors in [33] use 18-380 V, 150 W prototype in which voltage-lift capacitor technique is used that not only doubles the gain of this topology but also

reduces switches voltage stresses to 18.95% of the output voltage. Also, switches have been operated with duty ratio of 0.5 and 180° phase shift that nullify source current ripple. Furthermore, the proposed converter gives efficiency of 94.7% at full load. The authors in [34] present 24V to 38V, 100W prototype whose gain is increased using voltage lift technique. Also, its input inductor current ripple content is reduced to 9.66% to the total current magnitude due to interleaving technique. The proposed converter in full load operating condition gives efficiency of 92.5%. They also conclude that input current with no ripple and high voltage gain make this converter suitable for PV applications.

5.1.2 Non-isolated Quadratic Boost PFC Converter

In this topology, the term "Non-isolated" means a converter with no transformer. Non-isolated converters are light in weight and have low cost because of absence of transformer. Since no transformer is used to increase the voltage gain, there is a limitation of high voltage gain. Such type of topology finds its application in dc microgrid where isolation is not required and is well suited for low to medium power application [12] [35]. Authors in [12], [36], [38], [39] present different circuit configurations of non-isolated QBC to increase the voltage gain. Circuit configuration of [12] that uses switched capacitor and inductor to make new voltage boost cell is shown in figure 7.



Figure 7: Non-isolated Quadratic Boost PFC Topology [12]

Authors in [12] present 100W prototype which gives twice gain compared to conventional QBC by using simple circuit configuration with one switch. It was found that converter gives efficiency of 88%. In [36], 75W prototype is presented that gives high voltage gain compared to conventional OBC by adding capacitor, inductor and diode. Capacitor and diode also reduce voltage stresses on main switch. The simulation results also show that properly designed current and voltage give good results for converter's dynamic condition. In [37]. prototype of bidirectional OBC (can be used as quadratic boost and buck converter) is presented with four switches which are magnetically coupled to provide wide range conversion ratio. The proposed converter uses magnetic coupling of two inductor and a damping network which eliminate right half plan (RHP) zero problems and makes it capable of handling large ripple current in quadratic buck mode. Its bidirectional nature allows it to use in the application of battery charging, UPS, hybrid EV etc. In [38], authors analyze the performance of transformerless (nonisolated) single switch QBC with high voltage gain for photovoltaic system connected to grid. A detailed simulation and hardware analysis is done with MPPT for verifying converter's usage in PV application. In [39], 12V 40Wprototype is presented by combing QBC with voltage multiplier cell which provides high voltage gain, high efficiency of 88% and low stress on semiconductor devices. The proposed converter's construction is simple with single switch.

6. CONCLUSION

Harmonics generated during AC to DC conversion should be within limits set by IEEE and IEC and to meet these standards, passive PFC is not well enough. Active PFC using Boost PFC converter can reduce harmonics and provide high PF but it has drawback of ripples at output which can be overcome by using Quadratic Boost PFC converter. This paper literature review concludes that for high power applications along with improved power factor, there is a need of high voltage gain and using Boost PFC Converter with high duty cycle results in switching and conduction losses degrading converter's efficiency so Quadratic Boost PFC converter is well suited option. This study also shows that among all operating modes: CCM-CCM, CCM-DCM, DCM-CCM and DCM-DCM, Quadratic Boost Converter operating in DCM has the ability of self-power factor correction but with drawback of low efficiency due to current ripple. Controlling schemes i.e., PID, FOPID, Sliding Mode provide voltage regulation during line and load variations. Other controlling schemes like Variable duty cycle (VDC) results in high PF.Application of QBC in photovoltaic panel and electric vehicle fulfils the requirement of high output voltage. It is also concluded from the literature review that traditional and non-isolated (transformerless) topology is suitable for medium power application whereas interleaved topology is well suited for high power applications.

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