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Internal and External Leakage Current Decomposition of Surge Arresters Using Discrete Fourier Transform



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ABSTRACT

Surge arresters are over voltage protection devices that provide protection to power transmission and distribution systems from over voltages caused by lightning surges, switching operations in power systems and temporary over voltages caused by faults. However, they are subjected to aging due to environmental conditions and overheating. Therefore to avoid catastrophic failures of surge arresters, it is necessary to evaluate their performance either by online monitoring or offline testing of the resistive leakage current which is considered as one of most techniques for assessing the degradation level of surge arresters. This paper presents an efficient technique for decomposition of leakage currents of surge arresters using discrete Fourier transform. The paper aims to decompose the leakage current, either internal or external, into its resistive and capacitive components. After, separating the two current components, the third harmonic resistive current, which is considered an important indicator of SA degradation, is extracted from the resistive current component. In this paper, diagnosis of three different 33 kV, 10 kA distribution-class surge arresters, namely Zinc Oxide Polymeric-, Zinc Oxide Silicon Rubber- and Silicon Carbide Porcelain-housed surge arresters is presented. Both the internal and the external currents were measured for each arrester up to its maximum continuous operating voltage.

Key words: Aging, arrester, Fourier transform, leakage current, monitoring.

1. INTRODUCTION

Transmission and distribution overhead lines are usually protected against over voltages caused by lightning and switching surges as well as temporary over voltages [1]–[4]. One of the most effective and in-expensive ways to protect the transmission line from lightning overvoltage is by installing either Silicon Carbide (SiC) surge arresters (SAs) with spark gaps, SiC SAs with current limiting gaps and gapless Zinc Oxide (ZnO) SAs. Their function is to limit the magnitude of over voltages to a safe acceptable level below the basic insulation level (BIL) of equipment being protected by absorbing and dissipating its energy [5].

The SiC SA has some superior electrical characteristics. It has a very high nonlinear resistance under low voltage condition, but a very low resistance to high voltage conduction [5]–[6]. When an over voltage occurs, the SCi resistance breaks down allowing conduction of the internal leakage current (ILC) to ground. However, SiC SAs have some disadvantages that make it undesirable as an over voltage protection device as the SiC blocks are vulnerable to moisture; therefore it was replaced by ZnO SAs [7]. The ZnO SA is an arrester that is commonly used today's in power system over voltage protection .Unlike SiC SAs, the ZnO SAs have no airgaps (gapless) and therefore eliminate the heat associated with arcing discharges and offer more excellent characteristics [8]. Gapped arresters are not able to limit the switching overvoltage effectively. Therefore, the gapless ZnO SAs which exhibit high nonlinear voltage current (V-I) characteristics and faster conduction response for over voltage surges are developed [9]–[11]. The SiC SAs have been replaced by ZnO arresters but a large number of SiC arresters are still operating and in service [6].

Over the past years researchers have conducted many research studies related to aging and degradation of SAs by developing online and offline based monitoring techniques and indicators. Many methods for the determination of arresters' conditions have been presented such as V-I characteristic curve analysis [7], partial discharge and electromagnetic radiation measurement [12]–[13], ultrasonic and radio interference detection [14] and certainly the leakage current (LC) measurement where, the majority of diagnostic methods are based on the total leakage current (TLC) magnitude and harmonics measurements [15]–[32].

As a result of several investigations, it was noticed that the TLC of a SA was used for condition monitoring [15]–[26]. Furthermore, other research studies showed that the resistive component of TLC can be indicative about the level of arrester

deterioration. Consequently, several monitoring techniques which include modified shifted current, current orthogonality, field probe method and capacitive current compensation techniques were developed to accurately monitor and extract the resistive current component [1], [15]-[16], [27]-[32]. Moreover, some research studies focused on monitoring the third harmonic component of resistive leakage current (RLC) as a good aging indicator due to the strong relationship between SA aging and the level of third harmonic component [27]–[31]. This will be the main concern of this paper as developing a technique for monitoring the LC and extracting the third harmonic current is therefore, important to maintain reliability, economy, performance, and continuity of power system operation.

Online measurement techniques give fast assessment on the condition of SAs and can be used for the predictive maintenance. However, the disadvantage of online monitoring technique is that the measurement of the TLC is influenced by voltage harmonics. On the other hand, offline measurement techniques can be applied using voltage source that are suited for this purpose, the offline methods therefore have good accuracy [33].

In previous research works, the TLC combining the ILC and external leakage currents (ELC) were examined. In this paper a simple yet efficient offline technique for decomposition of the LC using discrete Fourier transform (DFT) is presented. The paper aims to decompose the LC, either ILC or ELC, into its resistive and capacitive components. After, separating the two current components, the third harmonic resistive current is extracted from the resistive current component. Three different 33 kV-10 kA distribution-class surge arresters, namely Zinc Oxide (ZnO) polymeric-, ZnO Silicon rubber (SiR)- and Silicon Carbide (SiC) Porcelain-housed SAs are tested. Both of the two ZnO SAs are new whereas the SiC SA is an aged one (15 years in service). Both the ILC and the ELC were measured for each SA up to its maximum continuous operating voltage (MCOV) [27]. The specifications of the SAs are provided the Appendix [27]. Matlab software has been used for analyzing the measured ILC and ELC signals.

2. EQUIVALENT CIRCUIT OF SURGE ARRESTER

A SA has high voltage and ground terminals and usually installed between phase and earth/neutral. Under normal operating conditions, the SA acts as an insulator, however, when get subjected to a transient over voltage greater than normal line voltage, the SA provides a low impedance path to ground and hence diverts the current through the semiconductor blocks to ground thus limiting the over voltage and avoid equipment damage or service interruption and then restore back to a normal condition. This current is called ILC. In addition, some current leaks to ground through the housing insulation, this is called ELC [9-11]. The SA is modeled by an equivalent circuit consistent of capacitive branch in parallel with a non-linear resistive branch as shown in Figure 1. Thus, the ILC or ELC of a SA comprises two components, namely capacitive component originated from the metal permittivity of the SA and the resistive component due to intrinsic resistance of the semiconductor blocks [15-21].

Practically, capacitive leakage current (CLC) component does not change. Therefore it is not important in the monitoring. On the contrary, the resistive leakage current (RLC) component varies with changes in SA characteristics, operating voltage, ambient temperature, lightning current discharge, and environmental factors. Accordingly, the RLC can give a good indicator of SA aging. Moreover, because of the nonlinear V-I characteristics of the SA, the RLC component implies odd harmonic current components that may cause overheating, deterioration and failure of SAs. In particular, the third harmonic current component has a strong relationship with aging. Therefore, it is considered the most important indicator of SA aging [19-26].



Figure 1: Equivalent Circuit of Surge Arrester

2.1 Mathematical Analysis

The ILC and ELC can be expressed as a sum of the capacitive and resistive current components as follows:

$$i_{i}(t) = i_{Ci}(t) + i_{Ri}(t)$$

$$i_{e}(t) = i_{Ce}(t) + i_{Re}(t)$$
(1)

where $i_i(t)$ and $i_e(t)$ are the measured ILC and ELC, respectively. Similarly, $i_{Ci}(t)$, $i_{Ri}(t)$, $i_{Ce}(t)$, and $i_{Re}(t)$ are the internal and external capacitive and resistive current components, respectively. $i_R(t)$ is the resistive current component including odd harmonics.

In offline leakage current measurements, the tests are usually conducted in a high voltage laboratory using high voltage AC source. The voltage source is used to generate a pure sinusoidal voltage v(t). Assume that $v(t) = V_m \sin(\omega t)$, the capacitive current $i_C(t)$ contains the fundamental component which is orthogonal to the applied voltage waveform, $i_C(t) = Cdv(t)/dt$, whereas resistive current $i_R(t)$ is in phase with the applied voltage waveform and includes odd harmonics. Accordingly, the ILC or ELC can be expressed as

$$i_{i}(t) = I_{Ci1}\cos(\omega t) + \sum_{k} I_{Rik}\sin(k\omega t)$$

$$i_{e}(t) = I_{Ce1}\cos(\omega t) + \sum_{k} I_{Rek}\sin(k\omega t)$$
(2)

where I_{Ci1} or I_{Ce1} is the peak value of the fundamental CLC and I_{Rik} or I_{Rek} is the peak value of the k^{th} harmonic RLC. Usually, the third harmonic resistive current is the dominant harmonic component.

2.2 Proposed Technique

In this work, the measured ILC and ELC and their associated harmonic spectrums for an MCOV voltage reference signal are measured. Using Fourier series expansion for a periodic waveform, the ILC and ELC waveforms can be expanded as

$$i_{i}(t) = \sum_{k} a_{ki} \cos(k\omega t) + \sum_{k} b_{ki} \sin(k\omega t)$$

$$i_{e}(t) = \sum_{k} a_{ke} \cos(k\omega t) + \sum_{k} b_{ke} \sin(k\omega t)$$
(3)

where a_{ki} , b_{ki} , a_{ke} , and b_{ke} are the real and imaginary Fourier coefficients of ILC and ELC waveforms. These coefficients can be calculated using discrete Fourier transform (DFT). For *N* samples of one cycle of the measured ILC or ELC $i_i(n) = \{i_{i0}, i_{i1}, ..., i_{iN-1}\}$ and $i_e(n) = \{i_{e0}, i_{e1}, ..., i_{eN-1}\}$, the Fourier coefficients of are calculated as

$$a_{ki/e} = \frac{2}{N} \sum_{n=0}^{N-1} i_{i/en} \cos\left(\frac{2\pi kn}{N}\right)$$

$$b_{ki/e} = \frac{2}{N} \sum_{n=0}^{N-1} i_{i/en} \sin\left(\frac{2\pi kn}{N}\right)$$
(4)

Considering the voltage waveform as a reference signal and comparing equations (2) and (3), one concludes that a_{ki} , b_{ki} , a_{ke} , and b_{ke} coefficients represent the k^{th} peak values of the capacitive and the resistive harmonic current components, respectively. Next, after calculating the Fourier coefficients, the internal or external capacitive current $i_{Ci/e}(t)$ and the resistive current $i_{Ri/e}(t)$ can be constructed as follows. If voltage source harmonics are negligible, then $i_{Ci/e}(t)$ and $i_{Ri/e}(t)$ can be obtained as follows.

$$i_{Ci}(t) = \sum_{k} a_{ki} \cos(k\omega t) \cong a_{1i} \cos(k\omega t)$$

$$i_{Ri}(t) = \sum_{k} b_{ki} \sin(k\omega t) = b_{1i} \sin(\omega t) + b_{3i} \sin(3\omega t) + \cdots$$

$$i_{Ce}(t) = \sum_{k} a_{ke} \cos(k\omega t) \cong a_{1e} \cos(k\omega t)$$

$$i_{Re}(t) = \sum_{k} b_{ke} \sin(k\omega t) = b_{1e} \sin(\omega t) + b_{3e} \sin(3\omega t) + \cdots$$
(6)

The approximation given in (5) and (6) is highly applicable for ZnO SAs

3. EXPERIMENTAL SETUP

The experimental arrangement for measuring the ILC and ELC of the tested SAs is shown in Fig. 2. A 240V/400kV, 200

kVA, 50 Hz high-voltage transformer was used. The SA was directly connected to the high voltage terminal of the transformer. A capacitive voltage divider with a ratio of 7:1 in addition to an AC peak voltmeter was used to measure the applied voltage. The ILC and ELC ($i_i(t)$ and $i_e(t)$) were measured using two shunt resistors of 1 k Ω and 100 k Ω , respectively. The voltage waveform was considered as reference signal. The ILC was separated from the housing ELC by applying a grease layer near the base of the SA as indicated in Figure 2 [27].



Figure 2: Test Circuit for the Measurement of both Internal and External Total Leakage Currents

4. RESULTS AND DISCUSSION

In this work, three different SAs were tested at an MCOV of 29 kV_{rms} for extracting the ILC and ELC and their corresponding harmonic components. The voltage, ILC and ELC signals were captured and stored by a digital storage oscilloscope in the *.CSV file form. The Matlab software was used for signal decomposition and harmonic estimation. Figure 3 shows the applied voltage waveform and its corresponding harmonic spectrum. It can bee seen that the voltage is almost pure sinusoid with negligible harmonics.



(b) Voltage Harmonic Spectrum

Figure 3: Applied Voltage Waveform and Harmonic Spectrum The measured ILC and ELC of the three tested SAs are displayed in Figure 4.



(c) ILC and ELC of Porcelain SiC SA

Figure 4: Internal and External Leakage Currents of Tested SAs

The current decomposition of the ILC and ELC of the three tested SAs into capacitive and are resistive components are depicted in Figure 5.



(a) Capacitive and Resistive Components of Polymer ZnO SA

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(b) Capacitive and Resistive Components of SiR ZnO SA



(c) Capacitive and Resistive Components of Porcelain SiC SA

Figure 5: Capacitive and Resistive Current Components of the Internal and External Leakage Currents of Tested SAs

The harmonic spectrum of the CLC and RLC components for the tested SAs are illustrated in Figure 6.



(a) CLC and RLC Harmonic Spectrum of Polymer ZnO SA

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(b) CLC and RLC Harmonic Spectrum of SiR ZnO SA



(c) CLC and RLC Harmonic Spectrum of Porcelain SiC SA

Figure 6: Harmonic Spectrum of the Capacitive and Resistive Internal and External Leakage currents of the Tested SAs

 Table 1: Peak values of the internal and external leakage currents

 and their capacitive and resistive components of Polymer ZnO SA

Harmonic	To Peak	tal (µA)	Capa Peak	citive (µA)	Resistive Peak (µA)			
Order	I _{int}	I _{ext}	I _{int}	I _{ext}	I _{int}	Iext		
1	196.7	8.88	194.3	8.77	30.94	1.49		
3	19.13	0.59	14.77	0.33	12.15	0.49		
5	8.66	0.17	6.78	0.11	5.38	0.14		
7	1.14	0.13	1.14	0.13	0.00	0.00		

Table 2: Peak values of the internal and external leakage currents and their capacitive and resistive components of SiR ZnO SA

	To	otal	Capa	citive	Resistiv	ve Peak
Harmonic	Peak	(µA)	Peak	(µA)	(μ	A)
Order	I _{int}	I _{ext}	I _{int}	Iext	I _{int}	Iext
1	409.5	5.14	391.1	5.11	121.3	0.52
3	74.81	0.32	40.03	0.32	63.19	0.06
5	30.89	0.13	12.21	0.13	28.11	0.01
7	0.74	0.048	0.74	0.048	0.00	0.00

Table 3: Peak values of the internal and external leakage currents and their capacitive and resistive components of Porcelain SiC SA

	To	otal	Capa	citive	Resistive Peak (µA)			
Harmonic	геак	(μΑ)	геак	(μΑ)				
Order	I _{int}	Iext	$\mathbf{I}_{\mathrm{int}}$	I _{ext}	$\mathbf{I}_{\mathrm{int}}$	I _{ext}		
1	401.8	12.25	71.74	0.885	395.3	12.21		
3	160.2	0.67	64.69	0.463	146.5	0.483		
5	31.18	0.16	23.55	0.159	20.43	0.017		
7	4.45	0.18	4.45	0.181	0.00	0.032		

In comparison with the RLC, Figs. 4-6 and Tables 1-3, reveal that for both the polymer and SiR ZnO SAs the internal and external fundamental CLC are the dominant components of ILC and ELC. It can also be observed that the magnitude of the third harmonic of internal RLC is greater than that of the CLC where the harmonics are almost small. Moreover, the harmonic currents of the external RLC are relatively lower for the SiR ZnO SA than for the polymer ZnO SA due to the high surface resistance of the SiR housing material. As for the Porcelain SA, it is

evident that the fundamental and third harmonic internal and external RLCs are larger than the CLCs. It can also be observed that both ILC and ELC have considerable combination of CLC and RLC components due the aging of the Porcelain SiC SA and the inherent lower degree of nonlinearity of SiC material [6], [10]. Furthermore, one can observe that for the three tested SAs the external CLC and RLC components are much lower than the internal CLC and RLC components. Therefore, the degradation of SAs can be specifically determined by monitoring and diagnosing the internal RLC component.

5. CONCLUSION

In this paper, decomposition of the internal and external leakage current into their capacitive and resistive components with their associated harmonic components using DFT-based technique for three different ZnO and SiC SAs was presented. Simulations results show that the proposed technique yield accurate estimation of the leakage current harmonic components rendering it as a useful and applicable tool for SA monitoring as resistive leakage current and the corresponding third harmonic current component provide adequate indicator of SA aging level.

APPENDIX

Specifications of the three tested surge arresters are provided in Table A.

Arrester	Statu s	Rated Voltage (kV)	Rated Current (kA)	MCOV (kV)
Polymer ZnO	New	36	10	27
Silicon rubber ZnO	New	36	10	29
Porcelain SiC	Used	36	10	-

Table A: Ratings of Tested Surge Arresters [27].

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